

FIG. 1

EXHIBIT 1

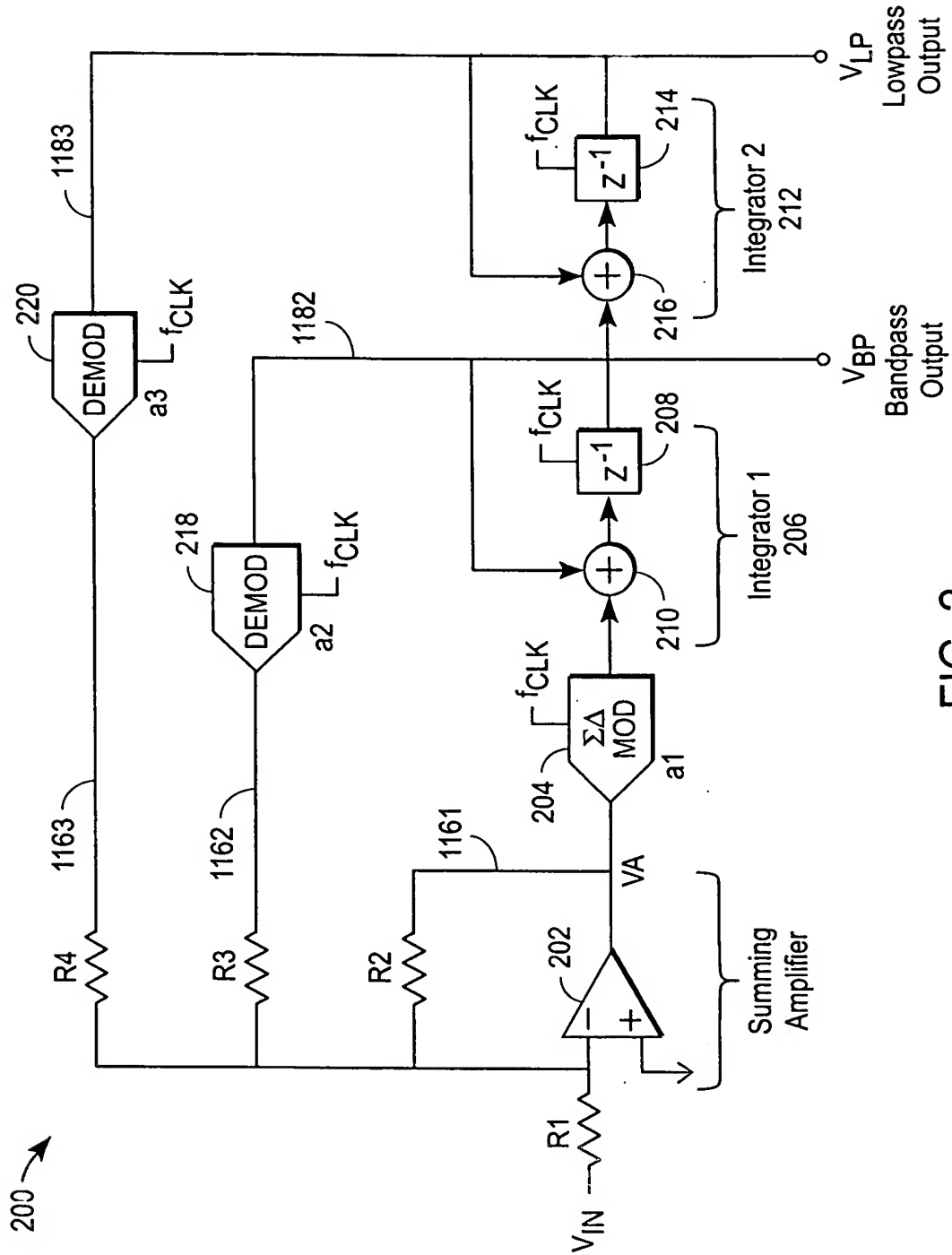
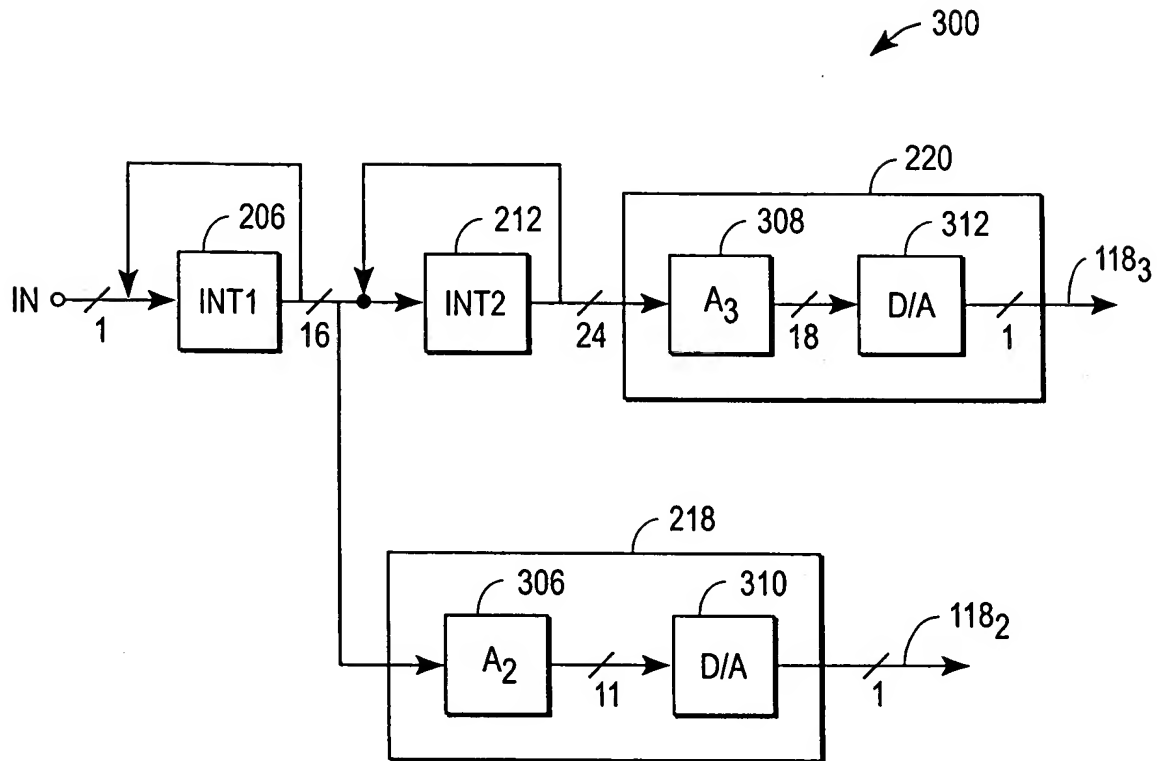
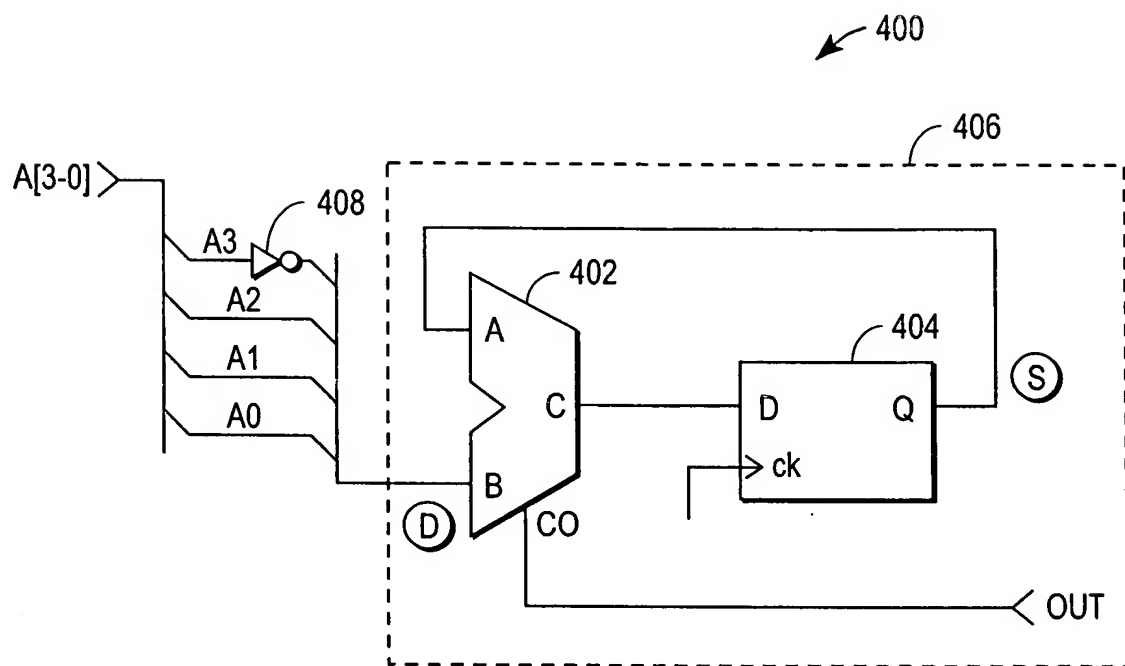


FIG. 2





Input: Two's Complement
Output: One Bit
HI = + 5V
LO = - 5V

FIG. 4

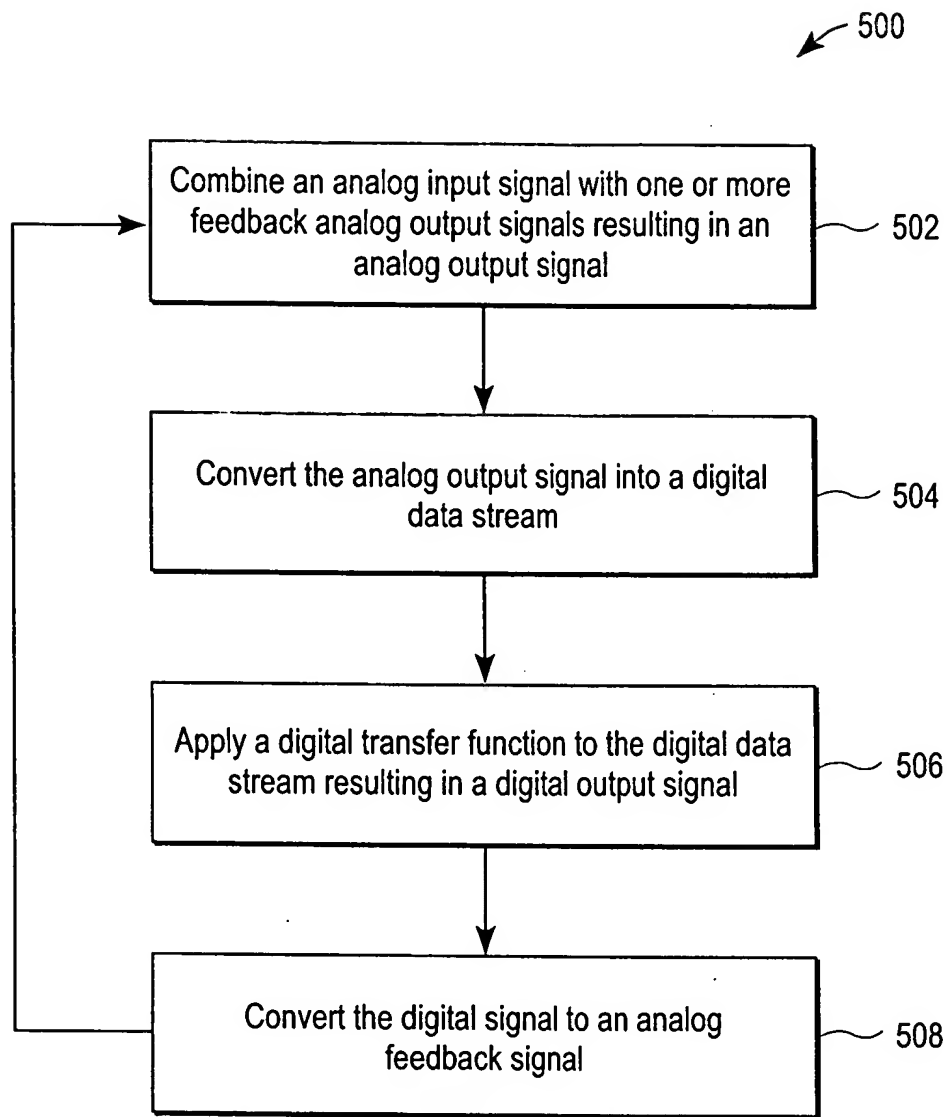


FIG. 5